

Title: Autotransformer-Based System and Method
of Current Harmonics Reduction in a Circuit
Inventor(s): Girish R. Kamath
Appln. No. Not yet assigned
Docket # 37125/31374

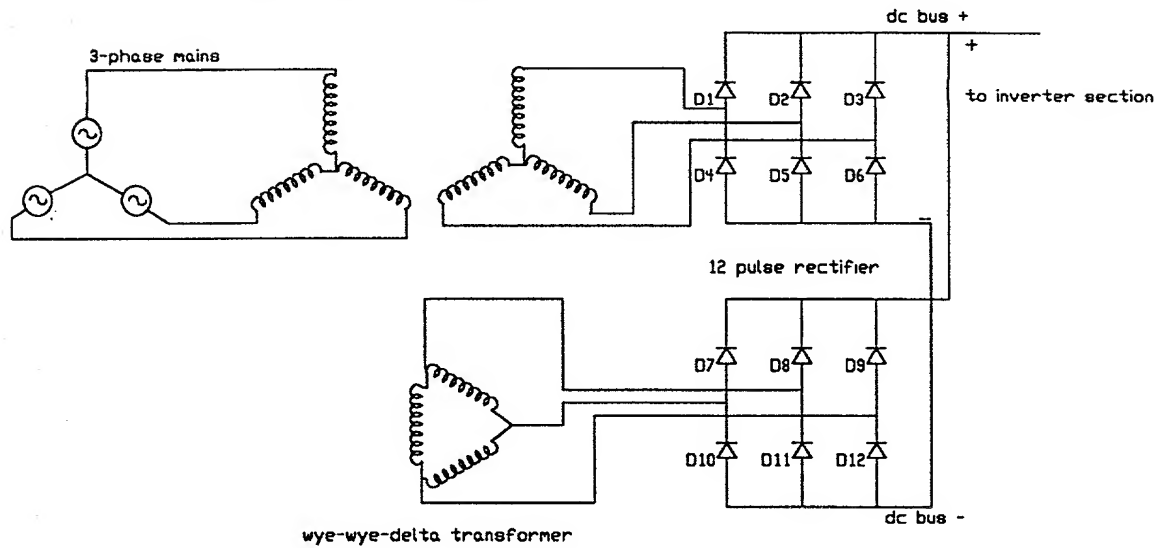


Fig. 1
Prior Art

1007475-000700

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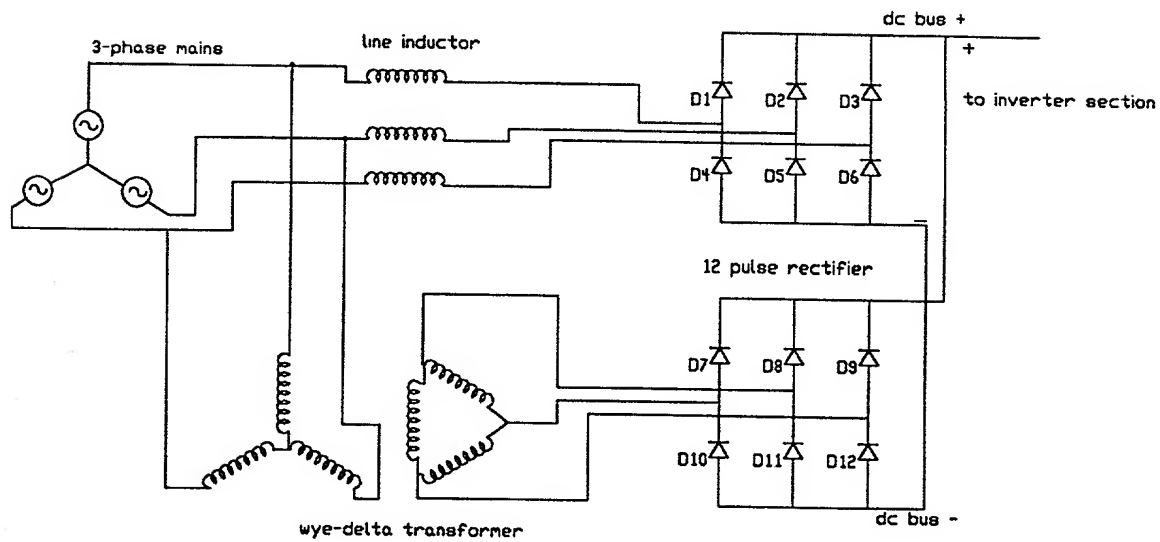


Fig. 2
 Prior Art

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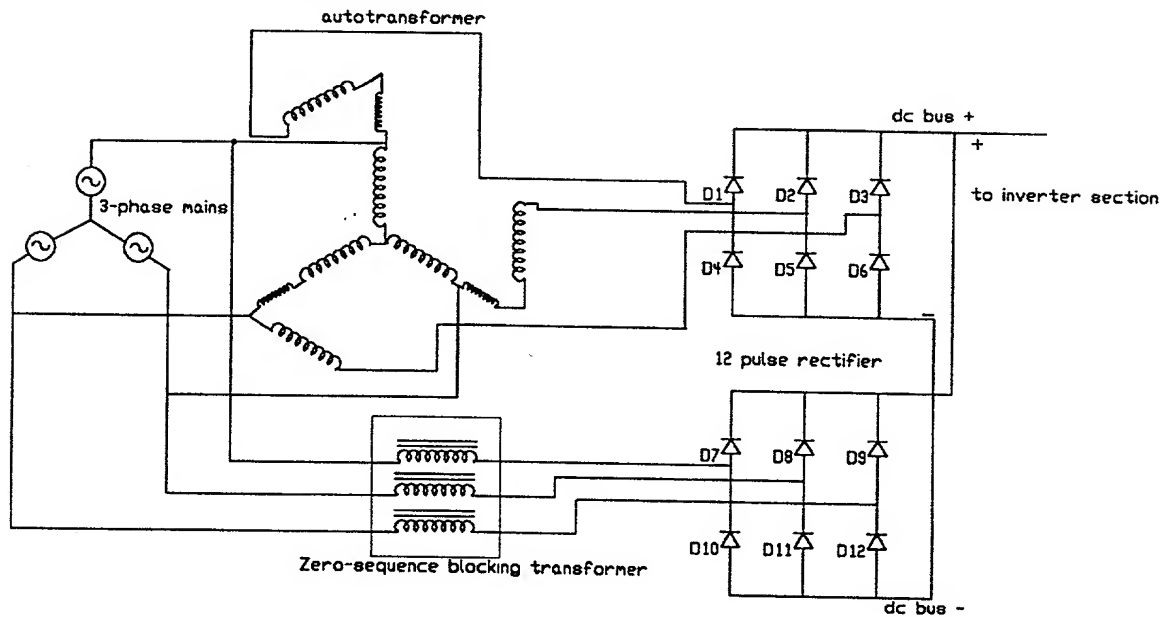


Fig. 3
Prior Art

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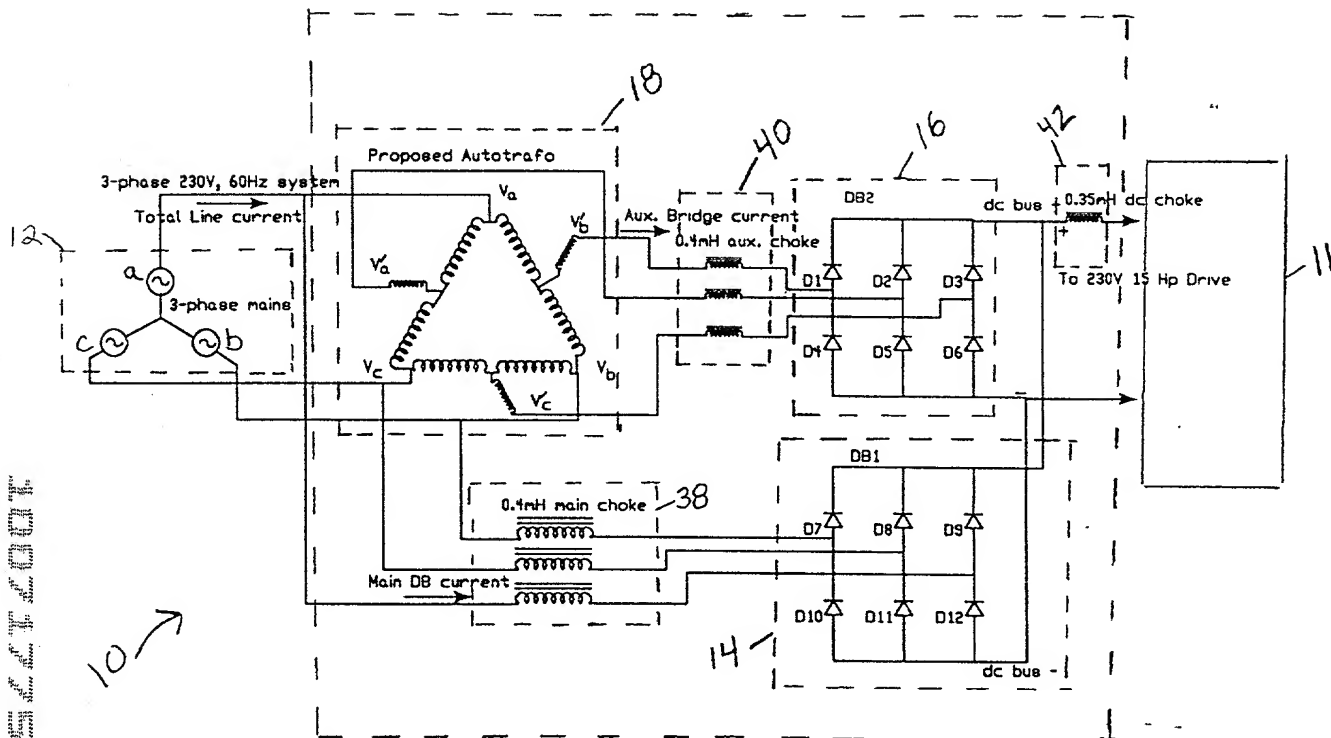


Fig. 4

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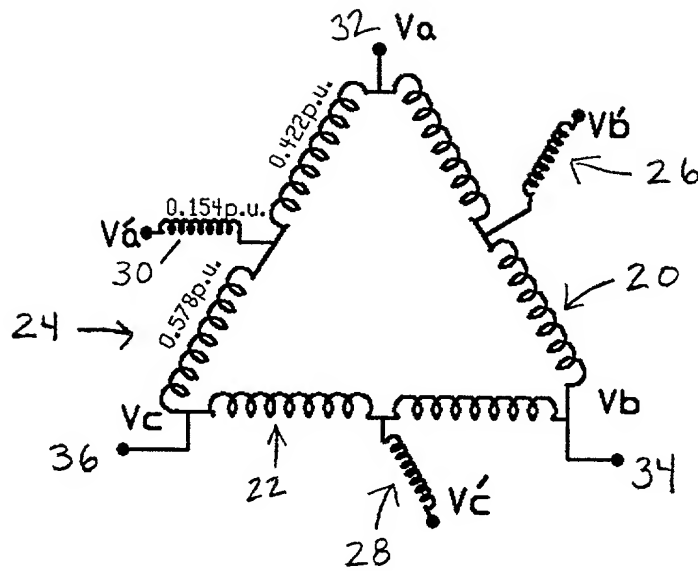


Fig. 5

20250205 1007475 020702

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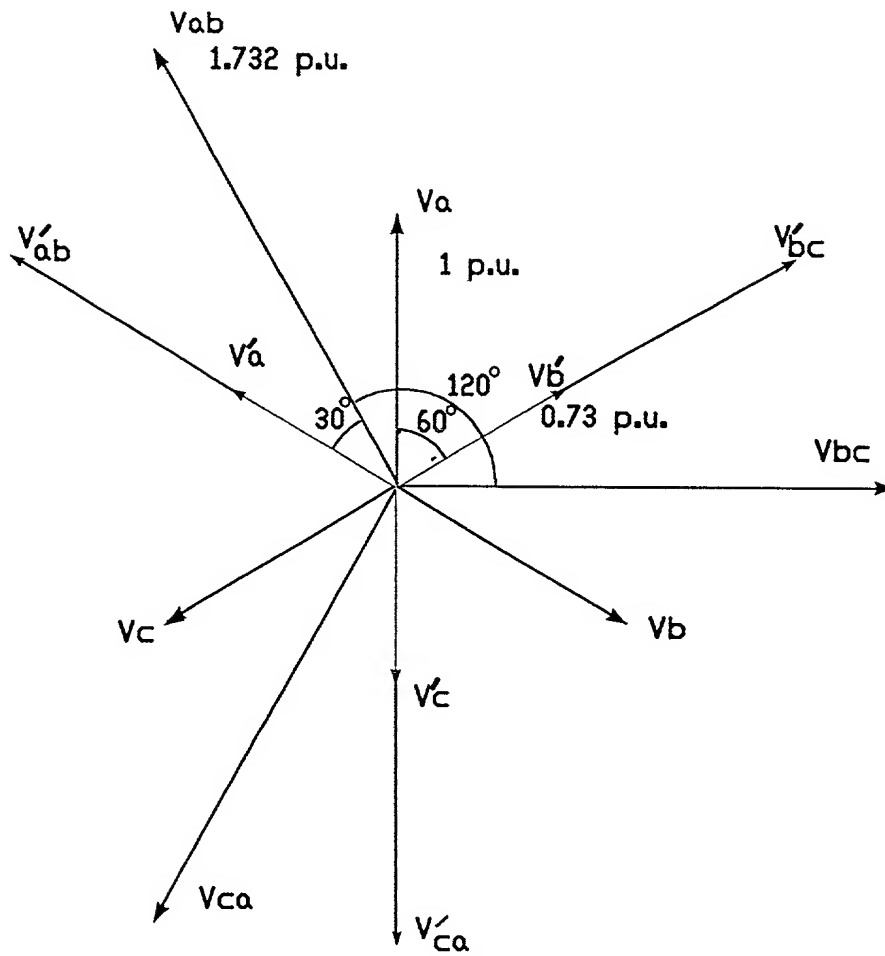


Fig. 6

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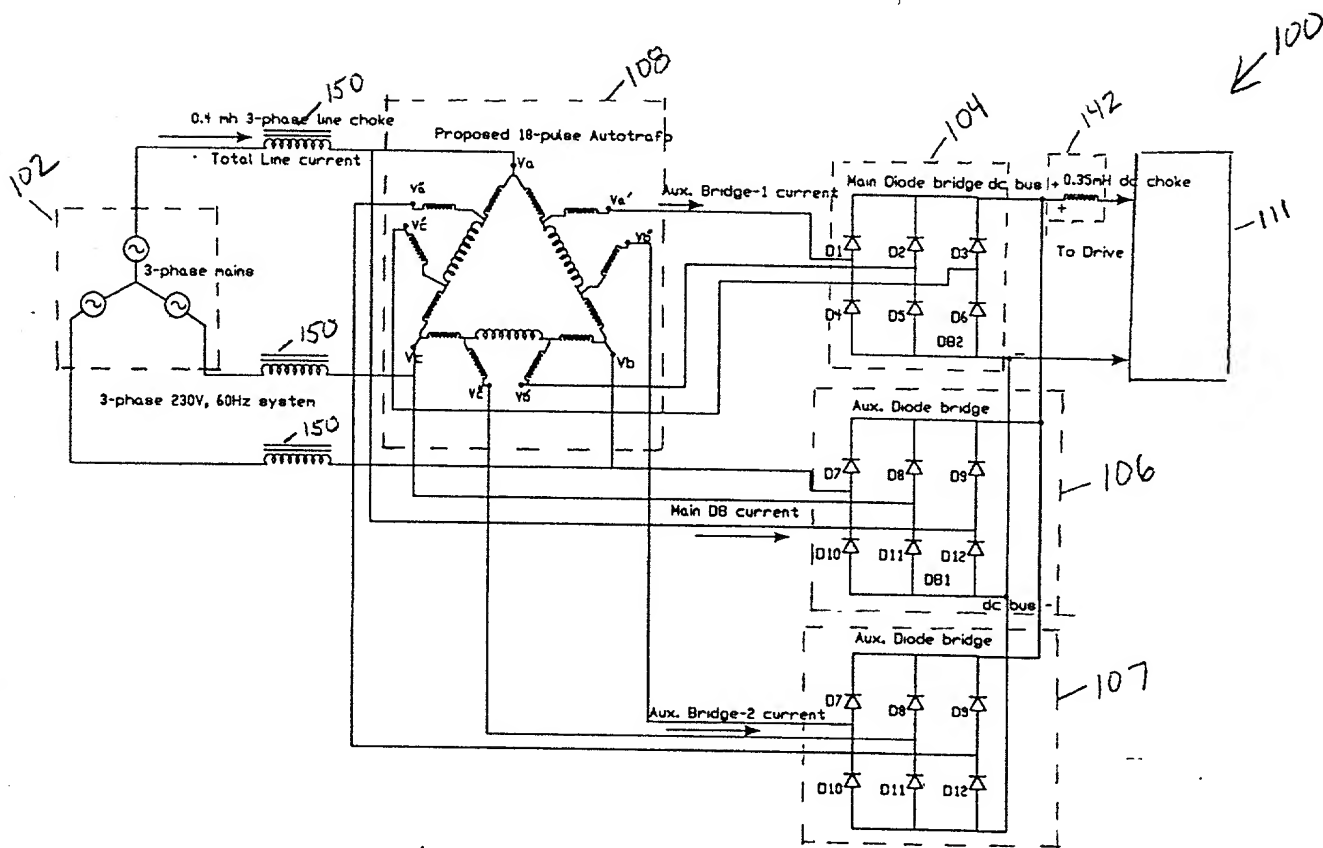


Fig. 7

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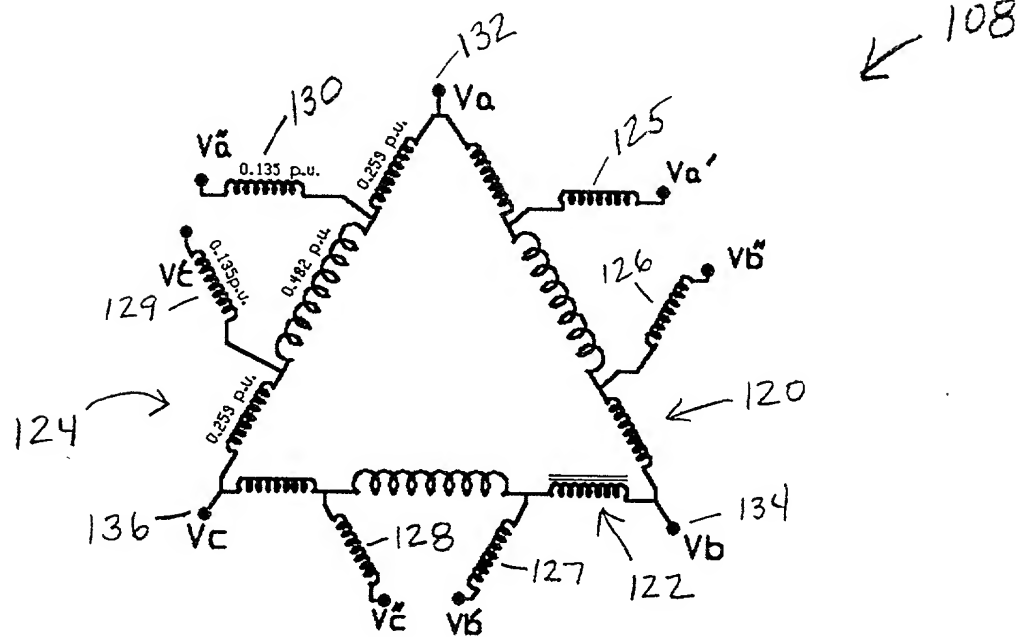


Fig. 8

20200522T001

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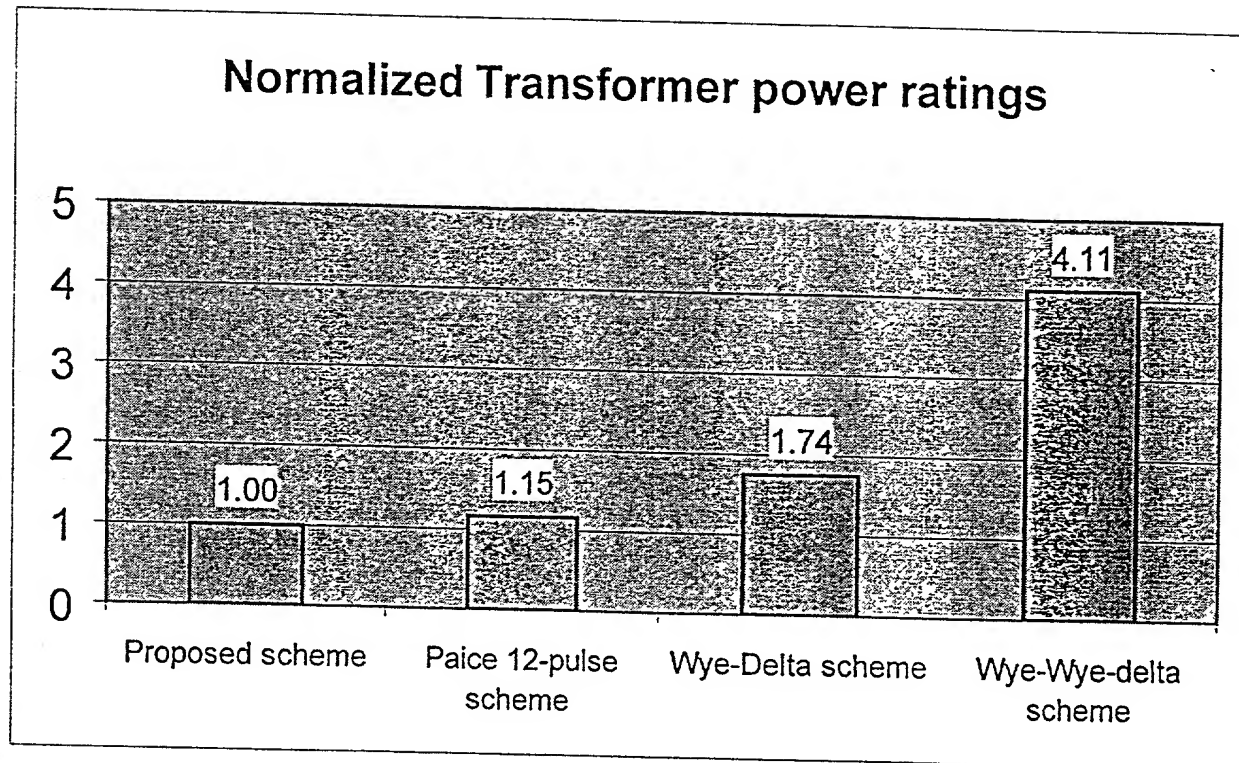


Fig. 9

20200524001

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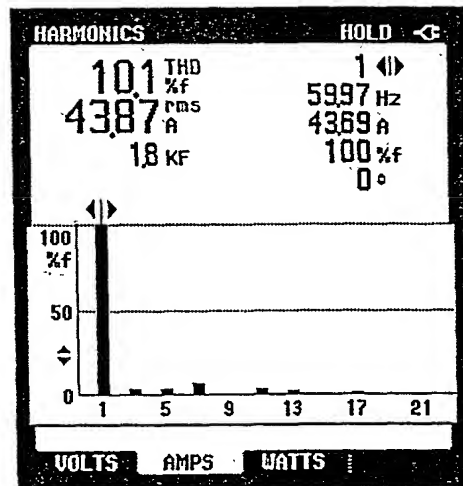


Fig. 10

2020 5/27/2021

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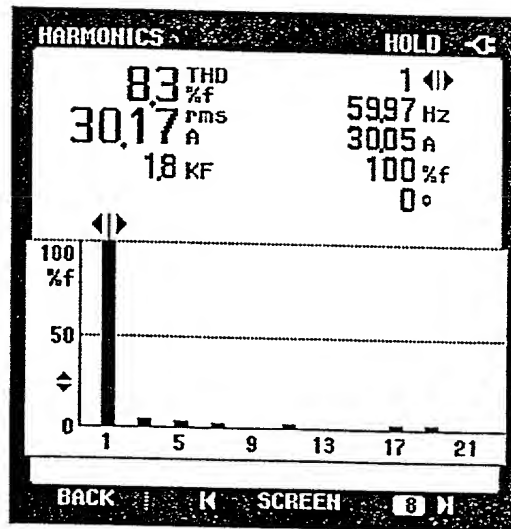


Fig. 11A

204020 5/5/2007

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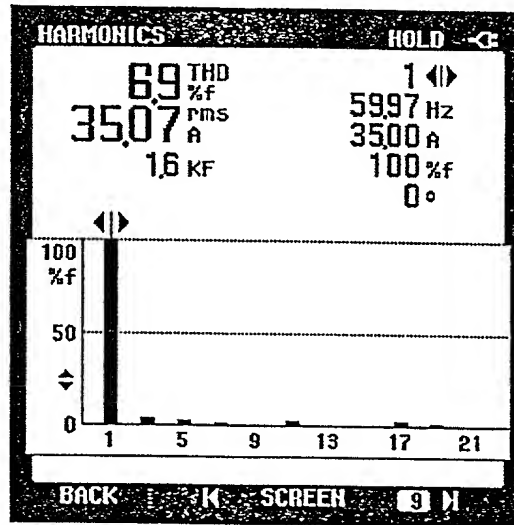


Fig. 11B

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